

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 12-18-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Characterizing Circuit Performance by
Separating Device and Interconnect
Impact on Signal Delay
Attorney Docket Number:: X-1447 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 07
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: P.R. China
Status:: Full Capacity
Given Name:: Xiao-Jie
Family Name:: Yuan
Street:: 1458 S. Mary Avenue
City:: Sunnyvale
State or Province:: California
Postal or Zip Code:: 94087

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: United Kingdom
Status:: Full Capacity
Given Name:: Michaél
Middle Name:: J.
Family Name:: Hart
Street:: 4047 Manzana Lane
City:: Palo Alto

State or Province:: California
Postal or Zip Code:: 95138

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Zicheng
Middle Name:: G.
Family Name:: Ling
Street:: 5505 Manderston Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95138

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Steven
Middle Name:: P.
Family Name:: Young
Street:: 1180 Pintail Circle
City:: Boulder
State or Province:: Colorado
Postal or Zip Code:: 80303

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124